

Crystal Oscillator SMD 5032 PECL/LVDS

Feature

- Typical 5.0 x 3.2 x 1.25 mm hermetically sealed ceramic package.
- Very low jitter performance: typical 0.3 pS RMS from 12 k - 20 MHz.
- Operating temperature up to 125°C
- Tri-state enable/disable



Size 5032

RoHS Compliant



REACH Compliant

Specifications

Parameter	PECL				LVDS				unit	
	3.3 V		2.5 V		3.3 V		2.5 V			
	Min.	Max.	Min.	Max.	Min.	Max.	Min.	Max.		
Supply Voltage Variation (V_{DD})	VDD-5%	VDD+5%	VDD-5%	VDD+5%	VDD-5%	VDD+5%	VDD-5%	VDD+5%	V	
Frequency Range	10	320	10	320	10	320	10	320	MHz	
Supply Current 10 MHz ≤ F _o < 160 MHz 160 MHz ≤ F _o < 250 MHz 250 MHz ≤ F _o < 320 MHz	—	75	—	75	—	50	—	50	mA	
	—	100	—	100	—	50	—	50		
	—	100	—	100	—	65	—	65		
Output Level Output High Output Low	2.275	—	1.475	—	—	1.6	—	1.6	V	
	—	1.68	—	0.88	0.9	—	0.9	—		
Transition Time: Rise/Fall Time	—	1.0	—	1.0	—	1.0	—	1.0	nSec	
Start Time	—	3	—	3	—	3	—	3	mSec	
Tri-State(Input to Pin 2 or Pin 1) Enable (High voltage or floating) Disable (Low voltage or GND)	2.31	—	1.75	—	2.31	—	1.75	—	V	
	—	0.99	—	0.75	—	0.99	—	0.75		
RMS Phase Jitter (Integrated 12 KHz ~ 20 MHz) F _o < 80 MHz 80 MHz ≤ F _o < 125 MHz 125 MHz ≤ F _o < 170 MHz 170 MHz ≤ F _o < 200 MHz 200 MHz ≤ F _o	—	1	—	1	—	1	—	1	pSec	
	—	0.5	—	0.5	—	0.5	—	0.5		
	—	0.3	—	0.3	—	0.3	—	0.3		
	—	0.5	—	0.5	—	0.5	—	0.5		
	—	0.3	—	0.3	—	0.3	—	0.3		
Phase Noise@ 156.25 MHz 100 Hz 1 kHz 10 kHz	—	-95	—	-90	—	-90	—	-90	dBc/Hz	
	—	-125	—	-125	—	-120	—	-120		
	—	-140	—	-140	—	-140	—	-140		
Aging (@ 25°C 1st year)	—	±3	—	±3	—	±3	—	±3	ppm	
Storage Temp. Range	-55	125	-55	125	-55	125	-55	125	°C	

Standard frequencies are frequencies which the crystal has been designed and does not imply a stock position.

+ Transition times are measured between 20% and 80% of V_{DD}.

Outline Drawing

